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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of  
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Serial No.: 09/800,145

Filed: March 6, 2001

For: Recursive Cryptoaccelerator and Recursive  
VHDL Design of Logic Circuits

Attorney's Docket No: 4015-815

Patent Pending

Examiner: Paula W. Klimach

Group Art Unit: 2135

Confirmation No.:4516

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATE OF MAILING OR TRANSMISSION [37 CFR 1.8(a)]**

I hereby certify that this correspondence is being:

- ☒ deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.
- ☐ transmitted by facsimile on the date shown below to the United States Patent and Trademark Office at (703) 273-8300.

August 26, 2005

Date

*Kathleen Koppen*  
Kathleen Koppen

RESPONSE TO NON-FINAL OFFICE ACTION

Applicant respectfully submits the following response in reply to the Office Action mailed May 27, 2005. Applicant requests reconsideration and reexamination in light of the remarks below. No fees should be required for entry of this response; however, if any fees or charges are required, the Commissioner is hereby authorized to charge them to Deposit Account 18-1167.